

NCEP40T15G

NCE N-Channel Super Trench Power MOSFET

Description

The NCEP40T15G uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

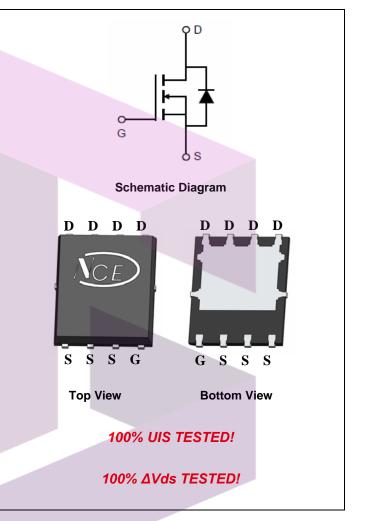
General Features

V_{DS} =40V,I_D =150A
R_{DS(ON)}=1.6mΩ (typical) @ V_{GS}=10V
R_{DS(ON)}=1.9mΩ (typical) @ V_{GS}=4.5V

- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|------------|----------------|-----------|------------|----------|
| NCEP40T15G | NCEP40T15G | DFN5X6-8L | - | - | - |

Absolute Maximum Ratings (T_c=25[°]C unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--|-----------------------|------------|-------------|
| Drain-Source Voltage | VDS | 40 | V |
| Gate-Source Voltage | Vgs | ±20 | V |
| Drain Current-Continuous (Silicon Limited) | I _D | 150 | A |
| Drain Current-Continuous(T _C =100℃) | I _D (100℃) | 106 | A |
| Pulsed Drain Current (Package Limited) | I _{DM} | 400 | A |
| Maximum Power Dissipation | PD | 88 | w v |
| Derating factor | 0 | 0.7 | W /℃ |
| Single pulse avalanche energy (Note 5) | E _{AS} | 720 | mJ |
| Operating Junction and Storage Temperature Range | T_{J}, T_{STG} | -55 To 150 | °C |





NCEP40T15G

Thermal Characteristic

| Thermal Resistance, Junction-to-Case ^(Note 2) | R _{θJC} | 1.42 | °C/W | ĺ |
|--|------------------|------|------|---|
|--|------------------|------|------|---|

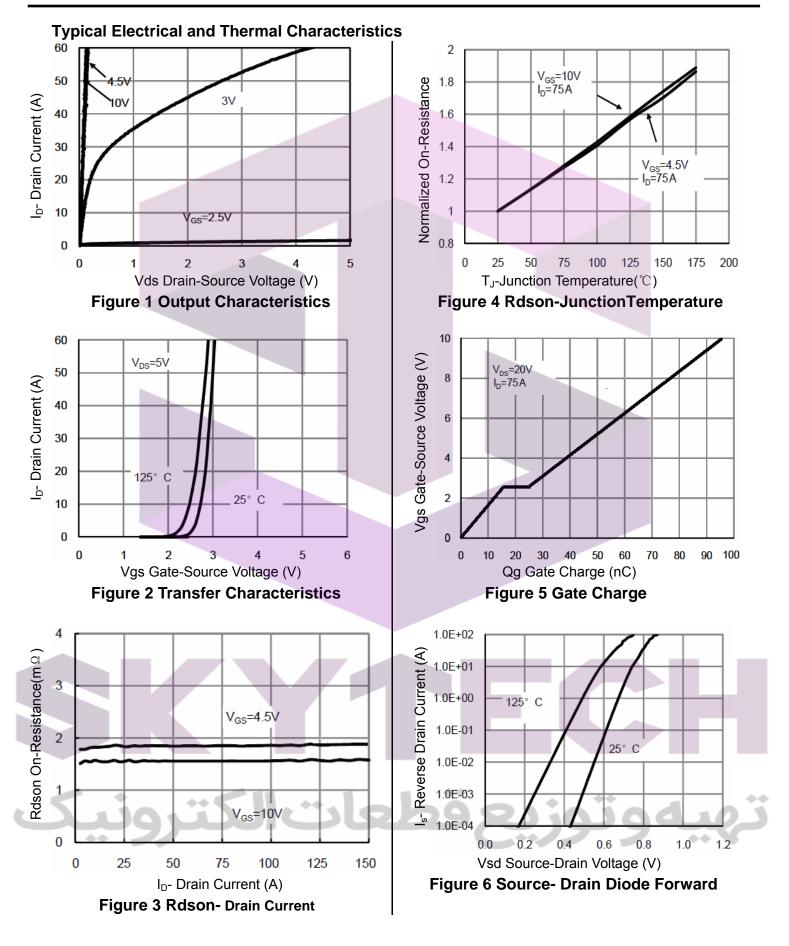
Electrical Characteristics (T_c=25[°]C unless otherwise noted)

| Parameter | Symbol | Condition | Min | Тур | Max | Unit |
|------------------------------------|---------------------|---|-----|------|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V I _D =250µA | 40 | | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =40V,V _{GS} =0V | - | - | 1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V,V _{DS} =0V | - | - | ±100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} ,I _D =250µA | 1.2 | 1.5 | 2.2 | V |
| Drain Source On State Desistance | | V _{GS} =10V, I _D =75A | - | 1.6 | 1.8 | mΩ |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =4.5V, I _D =75A | - | 1.9 | 2.3 | mΩ |
| Forward Transconductance | G FS | V _{DS} =5V,I _D =75A | | 80 | - | S |
| Dynamic Characteristics (Note4) | | | | | | |
| Input Capacitance | C _{lss} | | - | 6000 | 7150 | PF |
| Output Capacitance | Coss | V_{DS} =20V, V_{GS} =0V, | - | 1450 | 1700 | PF |
| Reverse Transfer Capacitance | Crss | F=1.0MHz | - | 100 | 145 | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | | - | 12.5 | - | nS |
| Turn-on Rise Time | tr | V _{DD} =20V,I _D =75A | - | 7.0 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | V _{GS} =10V,R _G =1.6Ω | - | 50 | - | nS |
| Turn-Off Fall Time | t _f | | - | 8.5 | - | nS |
| Total Gate Charge | Qg | | - | 95 | 115 | nC |
| Gate-Source Charge | Q _{gs} | V _{DS} =20V,I _D =75A, V _{GS} =10V | - | 15 | | nC |
| Gate-Drain Charge | Q _{gd} | V _{GS} =10V | - | 11 | | nC |
| Drain-Source Diode Characteristics | · | | | • | | |
| Diode Forward Voltage (Note 3) | V _{SD} | V _{GS} =0V,I _S =75A | - | | 1.2 | V |
| Diode Forward Current (Note 2) | Is | | - | - | 150 | Α |
| Reverse Recovery Time | t _{rr} | T _J = 25°C, I _F = I _S | - | | 31 | nS |
| Reverse Recovery Charge | Qrr | di/dt = 100A/µs ^(Note3) | - | | 110 | nC |

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production 5. EAS condition : Tj=25 $^{\circ}$ C,V_{DD}=20V,V_G=10V,L=0.5mH,Rg=25 Ω





Pb Free Product

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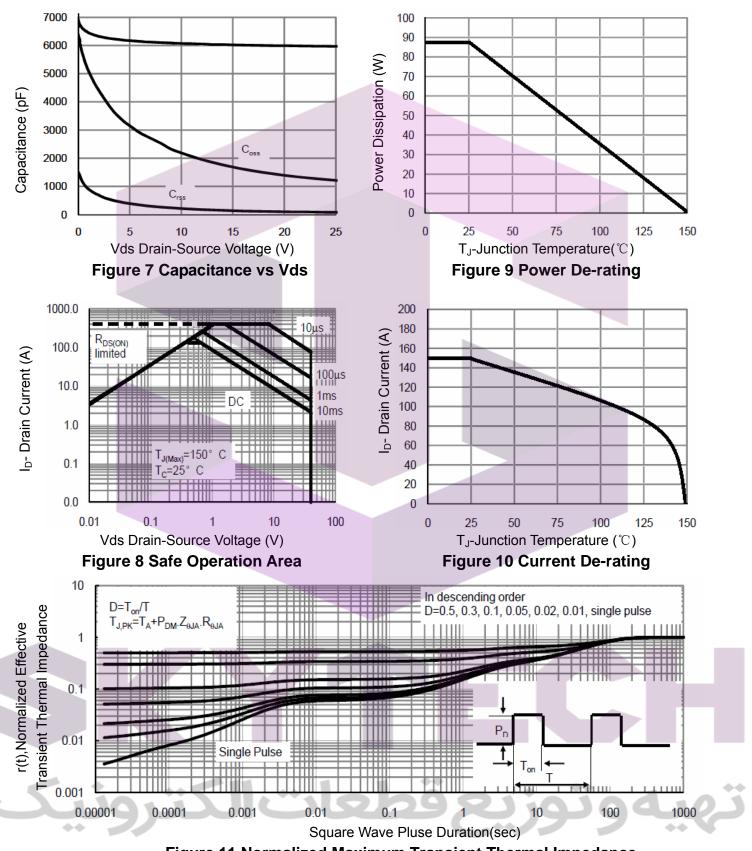


Figure 11 Normalized Maximum Transient Thermal Impedance

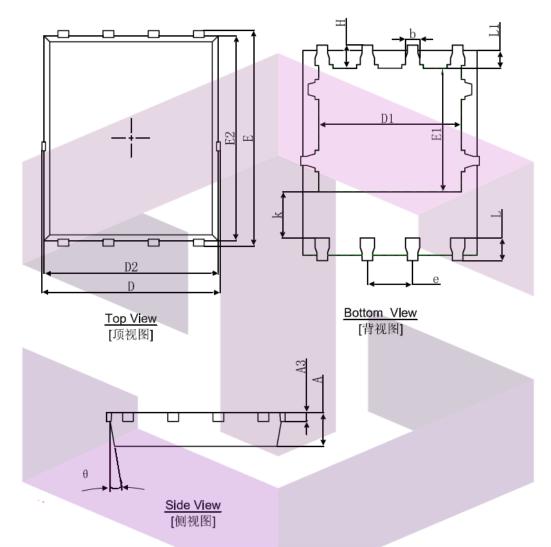


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DFN5X6-8L Package Information



| Sympol | Dimensions | In Millimeters | Dimensions In Inches | | |
|--------|------------|----------------|----------------------|-------|--|
| Symbol | Min. | Max. | Min. | Max. | |
| А | 0.900 | 1.000 | 0.035 | 0.039 | |
| A3 | 0.254 | IREF. | 0.010 | REF. | |
| D | 4.944 | 5.096 | 0.195 | 0.201 | |
| E | 5.974 | 6.126 | 0.235 | 0.241 | |
| D1 | 3.910 | 4.110 | 0.154 | 0.162 | |
| E1 | 3.375 | 3.575 | 0.133 | 0.141 | |
| D2 | 4.824 | 4.976 | 0.190 | 0.196 | |
| E2 | 5.674 | 5.826 | 0.223 | 0.229 | |
| k | 1.190 | 1.390 | 0.047 | 0.055 | |
| 🔶 b | 0.350 | 0.450 | 0.014 | 0,018 | |
| е | 1.270 | TYP. | 0.050TYP. | | |
| L | 0.559 | 0.711 | 0.022 | 0.028 | |
| L1 | 0.424 | 0.576 | 0.017 | 0.023 | |
| Н | 0.574 | 0.726 | 0.023 | 0.029 | |
| θ | 8° | 12° | 8° | 12° | |







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